

**Application
Number** **SEARCH****IDS Flag Clearance for Application** **IDS
Information**

| Content | Mailroom Date | Entry Number | IDS Review | Reviewer |
|---------|---------------|--------------|-------------------------------------|------------------------------|
| M844 | 03-24-2004 | 10 | <input checked="" type="checkbox"/> | 08-11-2005 00:56:55 IDS CONV |
| M844 | 06-08-2004 | 14 | <input checked="" type="checkbox"/> | 08-11-2005 00:56:55 IDS CONV |
| M844 | 10-12-2004 | 16 | <input checked="" type="checkbox"/> | 08-11-2005 00:56:55 IDS CONV |
| M844 | 01-10-2005 | 18 | <input checked="" type="checkbox"/> | 08-11-2005 00:56:55 IDS CONV |

UPDATE

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L5 | 46 | polysilicon same etch\$3 with (plasma wet) same (side wall) with (inclin\$3 taper\$3) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/01 10:45 |
| L6 | 31 | polysilicon same etch\$3 with (plasma wet) same (side wall) with angle | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/01 10:51 |
| L7 | 1210 | polysilicon with width same etch\$3 and (plasma wet) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/01 10:52 |
| L8 | 330 | polysilicon with width same etch\$3 same (plasma wet) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/01 11:45 |
| L12 | 404 | polysilicon with (taper decline angle) same etch\$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/01 11:54 |
| L13 | 359 | intel.as. and polysilicon and etch\$3 and (inclin\$3 taper\$3 width) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/01 11:56 |
| L14 | 95 | intel.as. and polysilicon same (inclin\$3 taper\$3 width) and etch | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/01 11:57 |